

IN THE CLAIMS:

*Please amend the claims as follows:*

1. (Currently Amended) A TFT array substrate, comprising:
  - a display area including a pixel electrode,
  - a switching element connected to the pixel electrode,
  - a gate line connected to the switching element and a source line connected to the switching element, and
  - a terminal formed outside of said display area for connecting the gate line ~~and~~ or the source line to wirings from an external signal source, wherein the terminal comprises a terminal electrode connected to said wirings from said external signal source, and a first metallic line and a second metallic line arranged beneath the terminal electrode, and each are connected to the terminal electrode via a contact hole,
  - wherein the first metallic line is formed in a side of the display area, and the second metallic line is formed in a side where the TFT array substrate is cut off ~~or~~ and chamfered off;
  - wherein the TFT array substrate further comprises an insulating layer which is interposed between the first metallic line and the second metallic line;
  - wherein the first metallic line is formed over the insulating layer;
  - wherein said second metallic line is formed beneath the insulating layer;
  - wherein any one of said first metallic line and said second metallic line is formed in the same layer of said source line, and the other one of said first and second metallic lines is formed in the same layer of said gate line;
  - wherein said first and second metallic lines are connected via a contact hole formed in a passivation film which is provided beneath the terminal electrode; and
  - wherein said second metallic line, which is formed in the side of the TFT array substrate which is cut off ~~or~~ and chamfered off, is formed beneath said both ~~gate insulating film layer~~ and said passivation film.
- 2-6. (CANCELLED)

7. (Previously Presented) The TFT array substrate of claim 1, wherein said first metallic line is connected with said source line.
8. (Previously Presented) The TFT array substrate of claim 1, wherein said second metallic line is connected with said gate line.
9. (Previously Presented) A display device comprising the TFT array substrate of claim 1.
10. (Previously Presented) A display device comprising the TFT array substrate of claim 7.
11. (Previously Presented) A display device comprising the TFT array substrate of claim 8.
12. (Previously Presented) The TFT array substrate of claim 1, wherein the first metallic line and the second metallic line are connected via a plurality of contact holes.